UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 7,053,888 B2 APPLICATION NO.: 10/054895

: May 30, 2006

DATED INVENTOR(S) : Muneki Ando

> It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

[56] <u>REFERENCES CITED</u>:

U.S. PATENT DOCUMENTS, "5,771,031 6/1998 Kinoshita et al." should read --5,771,031 1/1998 Kinoshita et al.-.

COLUMN 7:

Line 7, "time-senes" should read --time-series--.

Line 11, "senes" should read --series--.

COLUMN 21:

Line 46, "unit" should read --unit is (D - X - 1)/D2 times the capacity for one scan wiring .--.

COLUMN 29:

Line 26, "and (2)," should read -- and (2),

$$R1 + W1 + R2 + W2 = T(1 + 1/D)$$
 (3)--.

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Line 41, " $R1 + W1 \le T$ " should read --R1 + W1 < T--.

Line 43, " $R2 + W2 \le T$ " should read -- $R2 + W2 \le T$ --.

Line 49, " $n \le D$ " should read -n < D--.

COLUMN 34:

Line 33, "claims 1-9," should read --claims 1 to 16,--.

COLUMN 36:

Line 23, "nX/Dth" should read --nX/D + 1th--.

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COLUMN 39:

Line 15, "claims 32 to 36," should read --claims 32 to 39,--.

Signed and Sealed this

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First Day of January, 2008

JON W. DUDAS
Director of the United States Patent and Trademark Office